

TRANSMITTAL FORM

I hereby certify that this correspondence is being deposited with the United States Postal Service as "Express Mail" under Label No. EL 886 594 420 US in an envelope addressed to: Box Patent Application, Assistant Commissioner for Patents, Washington, D.C. 20231 on:

January 11, 2002
(Date of Deposit)

Kathy Dixon
Kathy Dixon

Box Patent Application
Assistant Commissioner
For Patents
Washington, D.C. 20231

Attorney Doc. #: 67,200-598
Mailing Date: Jan. 11, 2002

Dear Sir:

Transmitted herewith for filing is the patent application of:

Inventor(s): Yu-Ting Liao Fu-Sung Liu

For: Piercer Combined Prober For CU Interconnect Wafer-Level Preliminary Electrical Test

Submitted herewith are:

- ☒ 1 sheet of formal drawings showing Figs 1-4
- ☒ An Assignment of the invention to Taiwan Semiconductor Manufacturing Co., Ltd., together with Assignment Recordal Sheet
- ☒ A Declaration for patent application under CFR 1.63 and 1.68

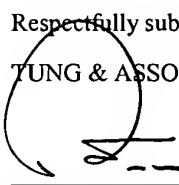
The filing fee has been calculated as shown below:

	No. Filed	No. Extra	Small Entity Fee	Large Entity Fee	Total
Basic Fee			\$370.00	\$740.00	\$740.00
Total Claims	26x20	6 x	\$9.00	\$18.00	\$108.00
Indep. Claims	4 -3	1 x	\$42.00	\$84.00	\$84.00
Multiple Dep. Clms.			\$140.00	\$280.00	\$0
Assign. Rec. Fee			\$40.00	\$40.00	\$40.00
TOTAL					\$972.00

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- ☒ Attached is a Credit Card Payment Form for the sum of \$ 972.00
- ☒ Charge Visa Credit Card No. 4756 8461 9568 0263 the sum of \$ 972.00
A duplicate copy of this transmittal is attached.
- ☒ The Commissioner is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Visa Credit Card No. 4756 8461 9568 0263.
A duplicate copy of this sheet is enclosed.
- ☒ Any additional filing fees required under 37 CFR 1.6
☒ Any patent application processing fees under 37 CFR 1.17

Respectfully submitted,
TUNG & ASSOCIATES



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RWT/kd
Enclosures